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(71) Applicant: CANON KABUSHIKI KAISHA
Tokyo (JP)

(72) Inventors:
• Mori, Toshihiro
Tokyo (JP)
• Ikeda, Masami
Tokyo (JP)
• Kasamoto, Masami
Tokyo (JP)

• Imanaka, Yoshiyuki
Tokyo (JP)
• Ozaki, Teruo
Tokyo (JP)
• Izumida, Masaaki
Tokyo (JP)

(74) Representative: Pellmann, Hans-Bernd, Dipl.-Ing.
et al
Patentanwaltsbüro
Tiedtke-Bühling-Kinne & Partner
Bavariaring 4
D-80336 München (DE)

(54) Liquid jet head substrate, liquid jet head using same and liquid jet apparatus using same

(57) A liquid jet element substrate having a plurality of ejection energy generating elements for generating ejection energy for ejecting liquid, arranged in an array in a direction at predetermined intervals, wherein an interval between the ejection energy generating element

at an end, in the direction of the array, and the ejection energy generating element adjacent thereto is smaller than an interval between adjacent central ejection energy generating elements.

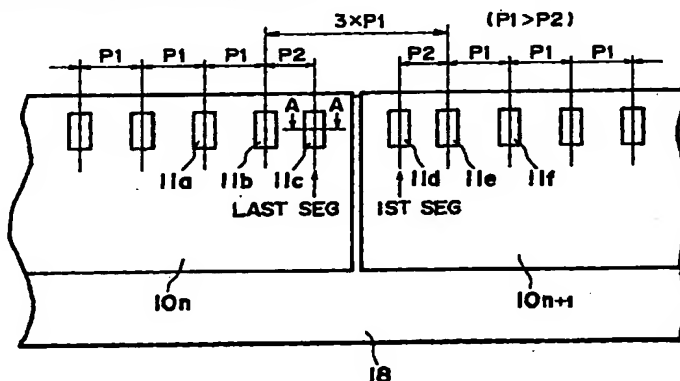


FIG. 1

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Description

FIELD OF THE INVENTION AND RELATED ART

The present invention relates to an element chip, which comprises an energy generating element for generating ejection energy to be used for ejecting recording liquid (ink or the like) in the form of a flying liquid droplet from an ejection outlet (orifice), and is employed in an ink jet head installed in an ink jet recording apparatus, which generates records by adhering the ejected liquid droplets to the recording medium. In particular, the present invention relates to such an element chip in which plural energy generating elements for generating the ink ejection energy to be used for ejecting the ink are arranged in a predetermined manner. The present invention also relates to an ink jet head, in which plural ejection energy generating elements are arranged in a predetermined manner, and an ink jet apparatus comprising such a head.

The ink jet recording method is a recording method in which ink (recording liquid) is ejected from an orifice, or orifices, of a recording head, so that the ejected ink is adhered to recording medium, such as paper, to create record. It has various advantages. For example, it generates only an extremely small amount of noise, and can record at a high speed. In addition, it can record on plain paper; it does not require dedicated paper with special composition. Therefore, various types of ink jet recording head have been developed.

Among them, there is a type which applies thermal energy to the ink to eject it from the orifice. This type of ink jet head is produced in the following manner. The electrothermal transducers and electrodes are formed on a substrate, and are covered with a protective film as needed. Then, a top plate, in which liquid paths and a liquid chamber are formed, is joined with the substrate.

The ejection energy for ejecting the ink from this type of recording head is generated by the electrothermal transducer comprising a pair of electrodes, and a heat generating resistor element disposed between the pair of electrodes. More specifically, an electric signal is applied to the electrode to cause the heat generating resistor element to generate heat. As heat is generated by the heat generating resistor, the ink adjacent to the heat generating resistor disposed within the ink path is instantaneously heated, generating bubbles. As the volume of the bubble quickly grows and contracts, the ink is ejected in the form of a liquid droplet.

When a recording head, which is structured as described in the foregoing, and is capable of accommodating an A3 paper, is wanted, plural element chips, in which a predetermined number of heat generating resistor elements are arranged at a predetermined pitch, are employed. More specifically, the plural element chips are precisely aligned on a supporting member, which has a width correspondent to the recording width, so that the recording width for A3 paper can be entirely covered with the aligned heat generating resistor elements, at the

same pitch as the heat generating resistor element pitch in each of the element chips.

However, the structure described above suffers from the following shortcoming. That is, in order to make the heat generating resistor element pitch, between the heat generating resistor elements located at each end of two adjacent element chips, substantially equal to the predetermined element pitch in each chip, both ends of each element chip must be cut at a point extremely close to a heat generating resistor element, during the element chip production.

As a result, the portions of the element chip, or, in the worst case, the heat generating resistor element itself, is liable to be damaged by chipping and/or shell cracking that could occur during the cutting process.

SUMMARY OF THE INVENTION

According to the present invention, which was made to eliminate the shortcoming described above, the heat generating resistor elements located near the end, relative to the alignment direction, of each element chip, are aligned at a smaller pitch than the normal (main) pitch for the heat generating resistor elements located across the middle of the same element chip; they are inwardly displaced, relative to the end of each element chip. With such placement of the heat generating resistor elements, the margin, which is reserved for cutting the substrate to separate each element chip, can be increased to prevent the heat generating resistor element from being damaged by chipping, shell cracking, and the like.

Further, when the above structure is not satisfactory, a stepped portion may be formed between the heat generating resistor element adjacent to the cutting margin, and the cutting margin, so that the effects of the aforementioned structure can be enhanced.

These and other objects, features and advantages of the present invention will become more apparent upon a consideration of the following description of the preferred embodiments of the present invention, taken in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a schematic view of an embodiment of the present invention.

Figure 2 is a schematic view of another embodiment of the present invention.

Figure 3 is a schematic view of a further embodiment of the present invention:

Figure 4 is a schematic sectional view of the embodiment of the present invention, illustrating a state of chipping which occurs when a substrate structured according to the present invention is cut.

Figure 5 is a schematic view of a conventional element chip, illustrating a state of chipping which occurs when a conventionally structured element chip is cut.

Figure 6 is a schematic view of another state of chipping which occurs when the conventionally structured element chip is cut.

Figure 7 is an exploded perspective view of an widened head, in which plural element chips in accordance with the present invention are aligned in a predetermined manner.

Figure 8 is a conceptual view of an ink jet recording apparatus employing a full-line head in accordance with the present invention.

Figure 9 is a perspective view of an ink jet recording apparatus employing the ink jet head in accordance with the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, the embodiments of the present invention will be described with reference to the drawings.

The phrase, "on the substrate," which is used in the following embodiments, means "on the substrate," as well as "immediately below the plane of the substrate surface."

Even though ink is used as the liquid to be ejected in the following embodiments, the liquid to be ejected is not limited to ink; any liquid is usable as long as it can be ejected by the ejection head in accordance with the present invention.

Figure 1 is a schematic view of an embodiment of the present invention. A reference numeral 11 designates a heat generating resistor element (ejection heater) as an ejection energy generating member. Each ejection heater comprises a heat generating resistor layer 12, and a pair of electrodes (unillustrated), and generates heat as a voltage is applied to the heat generating resistor layer 12 through the pair of electrodes. One of the electrodes is connected to an independent electrode (unillustrated), and the other is connected to a common electrode (unillustrated).

The heat generating resistor elements 11 are aligned on the element substrate at a predetermined pitch P1, except that the first and last heat generating resistor elements of each element chip, that is, the heat generating resistor element located at each end, in the alignment direction, of each element chip, is aligned at a shorter pitch P2 than those segments located between the first and last elements. Further, counting from left to right in Figure 1, the distance between the last element 11c of the first element chip, and the first element 11d of the next element chip is rendered greater than P1. Lastly, the distance between the second element 11b, counting from right to left, of the first element chip, and the second segment 11e, counting from left to right, of the next element chip, is set at a distance of approximately $3 \times P1$. Therefore, plural element chips can be aligned in a straight line, so that the alignment pitch for the heat generating resistor elements can be rendered substantially uniform across the entire length of the alignment.

Figure 2 is a schematic view of another embodiment of the present invention, in which three different pitches (P2, P3 and P4), which are shorter than the normal alignment pitch P1, are employed. In this drawing, the relationship among the different pitches is: $P1 > P2 > P3 > P4$. However, the relationship among the different pitches is not limited to the above. In other words, such factors as the number of alignment pitches different from the regular pitch P1, the positional relationship among the different pitches, and the like, may be optionally combined to obtain the same effect as the present invention.

In the embodiment illustrated in Figure 1, the distance between the second ejection heater, counting from left to right, of one element chip, and the second ejection heater, counting from right to left, of the next element chip, is set at approximately three times the pitch for the ejection heaters located at the center portion of the element chip. In the embodiment illustrated in Figure 2, the distance between the third ejection heater, counting from left to right, of one element chip, and the third ejection heater, counting from right to left, of the next element chip, is set at approximately seven times the pitch for the ejection heaters located at the center portion of the element chip.

With the arrangements described above, the element chip can be cut at a point close to the ejection heater, without damaging it; therefore, even when plural element chips are continuously aligned in a straight line, the ejection heater intervals can be rendered generally uniform.

The ejection heater intervals are not limited to those described above. Needless to say, the distance between the second ejection heaters of two adjacent element chips, counting away from the joint, may be set at approximately five times the interval between the adjacent ejection heaters located at the central portion of each element chip.

In the preceding embodiment, the interval between the adjacent two ejection heaters located near each end of each element chip is adjusted. However, when only two element chips are aligned, the ejection heater interval may be adjusted only at the element chip end on the joint side.

Figure 3 is a schematic section (at A-A line in Figure 1) of the embodiment of the present invention, illustrating a stepped portion 19 for preventing the advance of the crack, such as pitching or shell crack, which occurs while the substrate is cut. The stepped portion 19 can be formed using, for example, the same manufacturing step and the same material (Al, Cu or the like) for wiring electrode, without increasing the number of manufacturing steps. If cost is not a concern, the stepped portion 19 may be formed of a separate material (organic material such as polyimide).

Figure 4 is a schematic sectional view of the embodiment of the present invention, illustrating how the advance of the crack is prevented while the substrate is cut. Even if a crack 17 occurs as the chip substrate 10 is

cut across a margin 16, the advance of the crack can be stopped at the stepped portion 19.

Figures 5 and 6 are schematic sections of the conventional chip structure, illustrating how the crack advances while the substrate is cut.

As is evident from Figures 5 and 6, when the stepped portion 19 for crack advance prevention illustrated in Figure 4 is not provided, the crack spreads to affect the elements formed on the chip substrate.

The recording head described above can be produced following the steps described below.

To begin with, a 1 - 3 μm thick SiO_2 film as a heat storage layer 13 is formed on a Si wafer, using thermal oxidation. Next, a 400 - 2,000 \AA thick HfB_2 film which becomes the heat generating resistor layer, a 10 - 100 \AA thick Ti film which becomes an adhesion enhancement layer, and a 3,000 - 10,000 \AA thick Al (wiring electrode material), are deposited in this order by sputtering. Then, the heat generating resistors, electrodes, and the like, of desired patterns are formed by photolithography.

Next, a 1 - 2 μm thick film of SiO_2 or Si_3N_4 as a protective layer 14 is formed by CVD or sputtering. Thereafter, a 2,000 - 5,000 \AA thick Ta film as a cavitation resistance layer 15 is deposited by sputtering. Then, the desired patterns are formed by photolithography to complete the element chip 10.

The element chips 10 are precisely aligned on a supporting member 18 (for example, Al substrate) with excellent heat radiating properties, and fixed thereto by die bonding.

Lastly, a glass plate (unillustrated), which has grooves for forming at least the ink paths and orifices, is aligned on the chip substrate, so that the groove portions for forming the ink paths are properly located in relation to the heat generating portion formed on the chip substrate, and is glued thereto.

Instead, the walls for forming at least the ink paths and ejection orifices, may be formed on the chip substrate by photolithography which uses photosensitive resin or the like, and then, the walls may be covered to complete the recording head.

In the preceding embodiment, two element chips are aligned. However, a much larger number of element chips may be aligned to lengthen the recording head. Figure 7 illustrates such an example, in which plural element chips 100, in which plural heat generating resistors 101 are aligned in a straight line, are aligned in a straight line on a supporting member (base plate) of aluminum (Al) or the like. Each element chip is connected to the contact pad of the wiring chip through a connector 102. The top plate 200, which is grooved to form an ink path for each heat generating resistor, is attached to the plural element chips aligned as described above, to complete a wider head.

Figure 8 is a schematic perspective view of a so-called full-line type recording head, the width of which corresponds to the recording width of the recording medium, and a recording apparatus, in which the full-line type recording head is mounted. The present invention

displays the most outstanding effects when applied to the full-line recording head illustrated in Figure 8.

Referring to Figure 8, a reference numeral 6 designates a fill-line recording head. The ink is ejected from this recording head, in response to signals supplied from driving signal supplying means (unillustrated), toward a recording medium 80 such as paper or fabric conveyed by a conveyer roller 90, whereby recording is made on the recording medium 80. According to the present invention, even when a widened extended recording head such as the full-line head is employed, high quality recording can be easily made.

Figure 9 shows such a recording apparatus that employs a small recording head comprising only one or two element chips. The recording apparatus illustrated in Figure 9 comprises a recording head cartridge constituted of an independently exchangeable ink container 70 and an independently exchangeable recording head portion 60. It also comprises: a motor 81 as a driving power source, which drives the carriage; a conveyer roller 90 for conveying a recording medium 80; and a carriage shaft 85 for transmitting the driving force from the driving power source to the carriage. Further, it comprises signal supplying means for supplying an ink ejection signal to the recording head.

As described above, according to the present invention, even in the case of manufacturing a small element chip which requires cutting the chip substrate at a point close to the region in which the heat generating resistors are disposed, no damage occurs to the heat generating resistor. Therefore, even when plural element chips are aligned in a straight line, the heat generating resistor pitch can be rendered substantially uniform across the entire length of the alignment, satisfying the condition for the heat generating resistor alignment.

As is evident from the foregoing, according to the present invention, even when plural element chips are employed, the ejection heater pitch can be rendered substantially uniform across the combined length of the plural chips.

Further, the present invention also enjoys an advantage in that the element chip in accordance with the present invention can be manufactured using the conventional process, without a need for increasing the number of manufacturing steps; therefore there is no cost increase.

Further, when the chip substrate is cut to yield element chips, it can be cut at a point close to the heat generating resistor; therefore, plural element chips can be aligned to produce a wider recording head.

Consequently, the wider recording head can be inexpensively produced with extremely high yield.

When the head described is employed, an ink jet apparatus capable of recording high quality images at a high speed can be inexpensively produced.

While the invention has been described with reference to the structures disclosed herein, it is not confined to the details set forth, and this application is intended to cover such modifications or changes as may come within

the purposes of the improvements or the scope of the following claims.

A liquid jet element substrate having a plurality of ejection energy generating elements for generating ejection energy for ejecting liquid, arranged in an array in a direction at predetermined intervals, wherein an interval between the ejection energy generating element at an end, in the direction of the array, and the ejection energy generating element adjacent thereto is smaller than an interval between adjacent central ejection energy generating elements.

Claims

1. A liquid jet element substrate having a plurality of ejection energy generating elements for generating ejection energy for ejecting liquid, arranged in an array in a direction at predetermined intervals, wherein
an interval between the rejection energy generating element at an end, in the direction of the array, and the ejection energy generating element adjacent thereto is smaller than an interval between adjacent central ejection energy generating elements.
2. A substrate according to Claim 1, wherein an interval between a second ejection energy generating element from the end and a third ejection energy generating element is smaller than an interval between adjacent central ejection energy generating elements.
3. A substrate according to Claim 1 or 2, wherein said ejection energy generating elements are heat generating resistors.
4. A substrate according to Claim 1 or 2, wherein the end is each of the opposite ends of the array.
5. A substrate according to Claim 2, wherein said end is only one end of the opposite ends of the array.
6. A liquid ejection head comprising:
a plurality of element substrates each having a plurality of ejection energy generating elements for generating ejection energy for ejecting liquid, arranged in an array in a direction at predetermined intervals, wherein an interval between the ejection energy generating element at an end, in the direction of the array, and the ejection energy generating element adjacent thereto is smaller than an interval between adjacent central ejection energy generating elements; and
liquid flow paths corresponding to the respective discharging energy generating elements and each having an ejection outlet.

7. A liquid jet head according to Claim 6, wherein a plurality of said element substrates are continuously arranged on a support member, and wherein an interval between a second ejection energy generating element from the end of a first element substrate and a second ejection energy generating element from the end of the second energy generating element, is approx. 3 times an interval between central ejection energy generating elements.
8. A liquid jet head according to Claim 6, wherein a plurality of said element substrates are continuously arranged on a support member, and wherein an interval between a third ejection energy generating element from the end of a first element substrate and a third ejection energy generating element from the end of the second energy generating element, is approx. 5 times an interval between central ejection energy generating elements.
9. A liquid jet head according to Claim 6, 7 or 8, wherein the liquid ejection head is formed by coupling said element substrate with a member having grooves for constituting said plurality of liquid flow paths.
10. A liquid jet head according to Claim 9, wherein the groove members an elongated member common to the plurality of element substrates.
11. A liquid jet head according to claim 6, wherein said ejection energy generating elements include heat generating resistors.
12. A liquid jet head according to Claim 6, wherein ink is supplied to said liquid flow path.
13. A liquid ejecting device comprising:
the liquid ejection head as defined in Claim 6:
means for transporting a recording material.

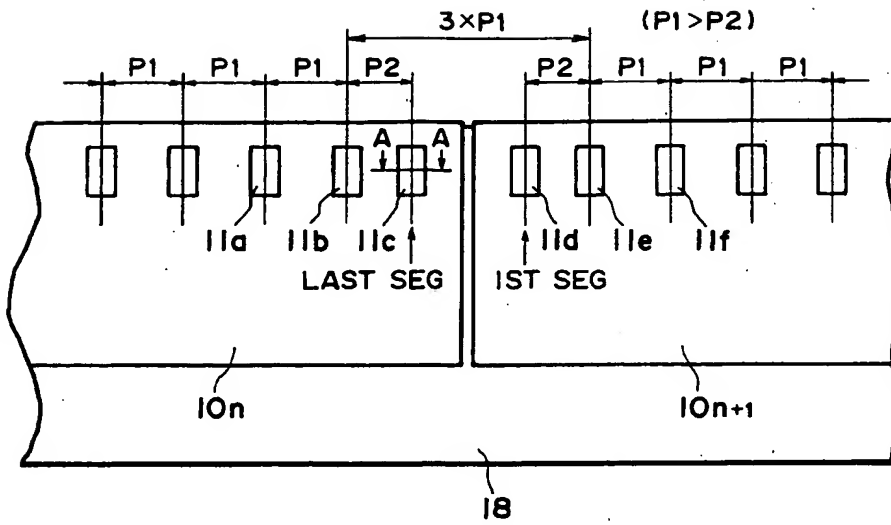


FIG. 1

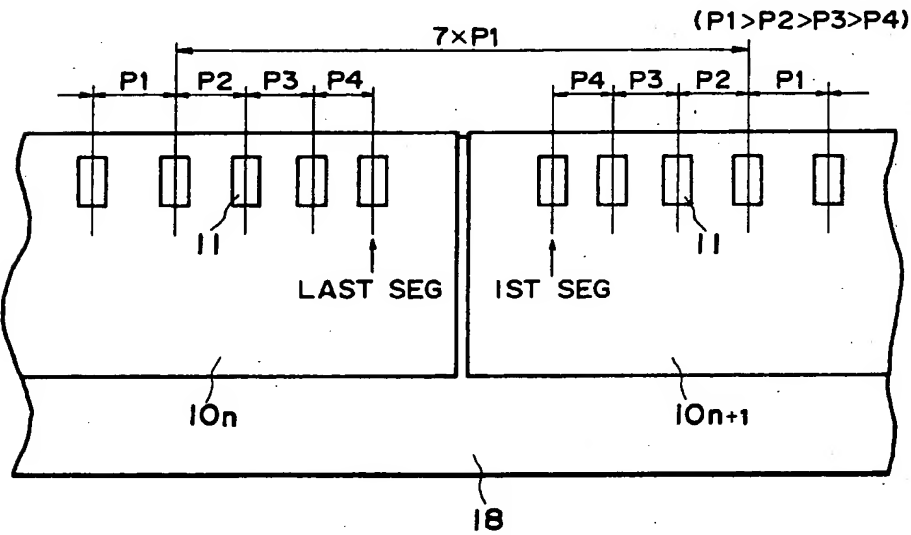


FIG. 2

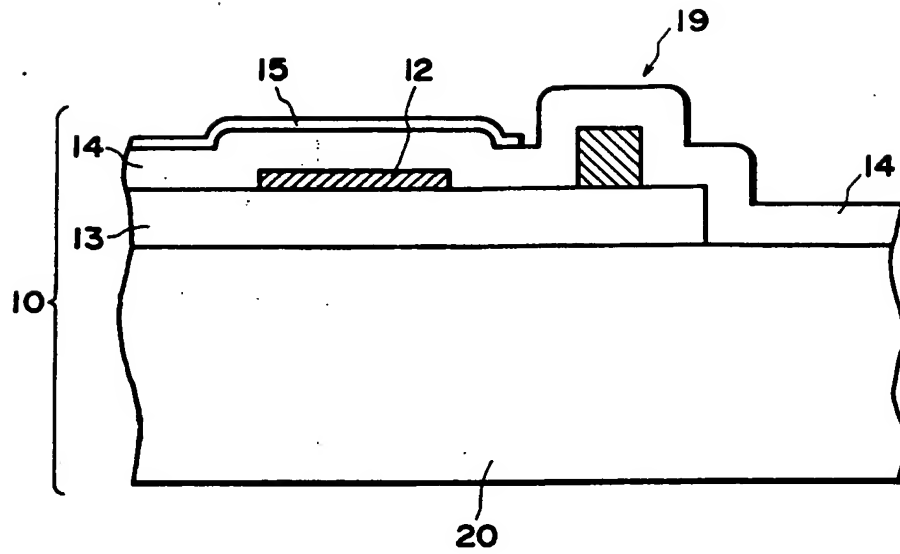


FIG. 3

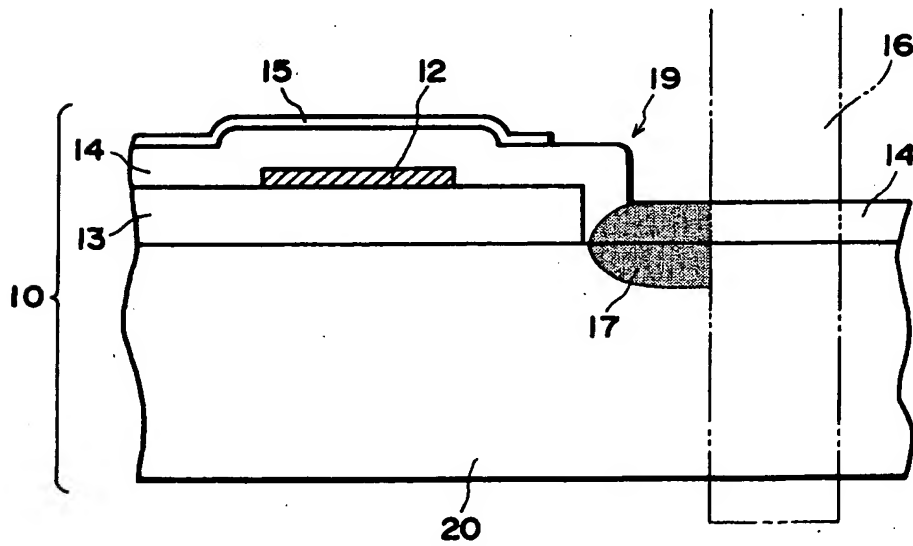


FIG. 4

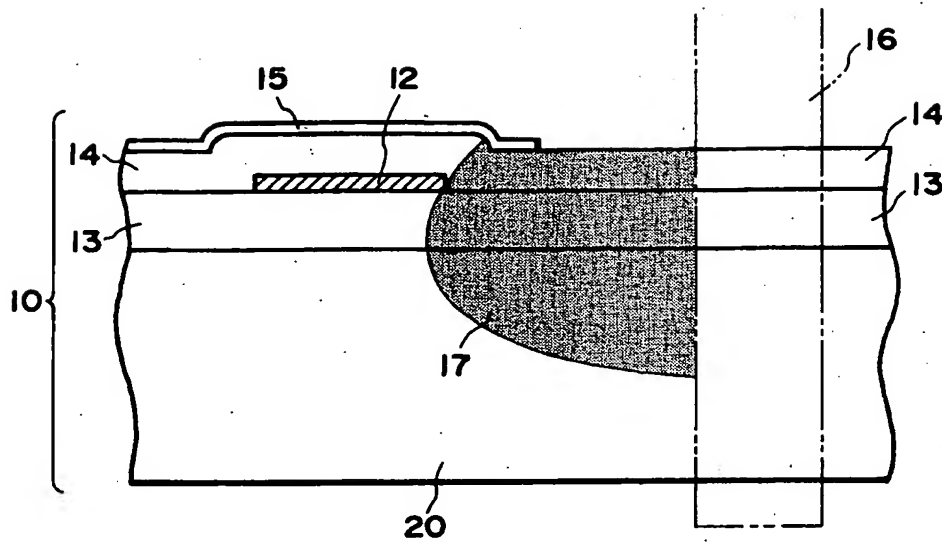


FIG. 5

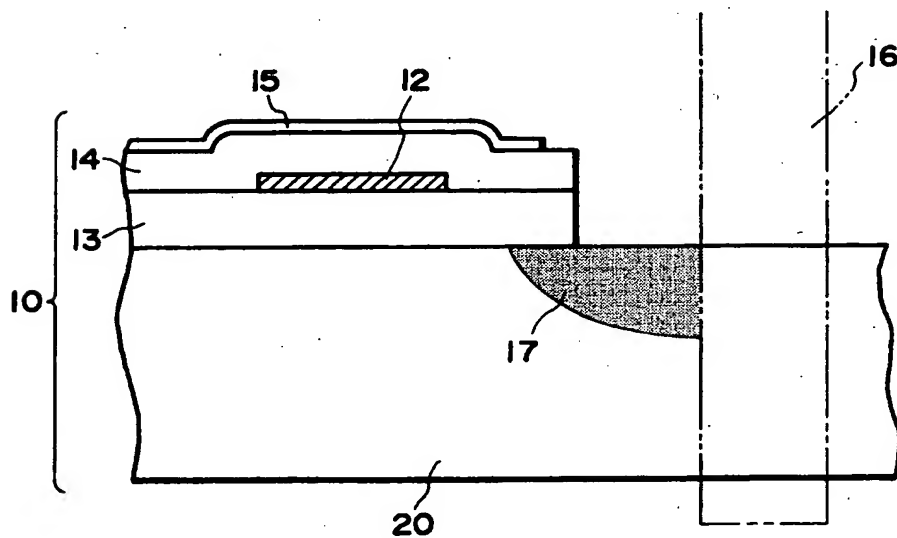
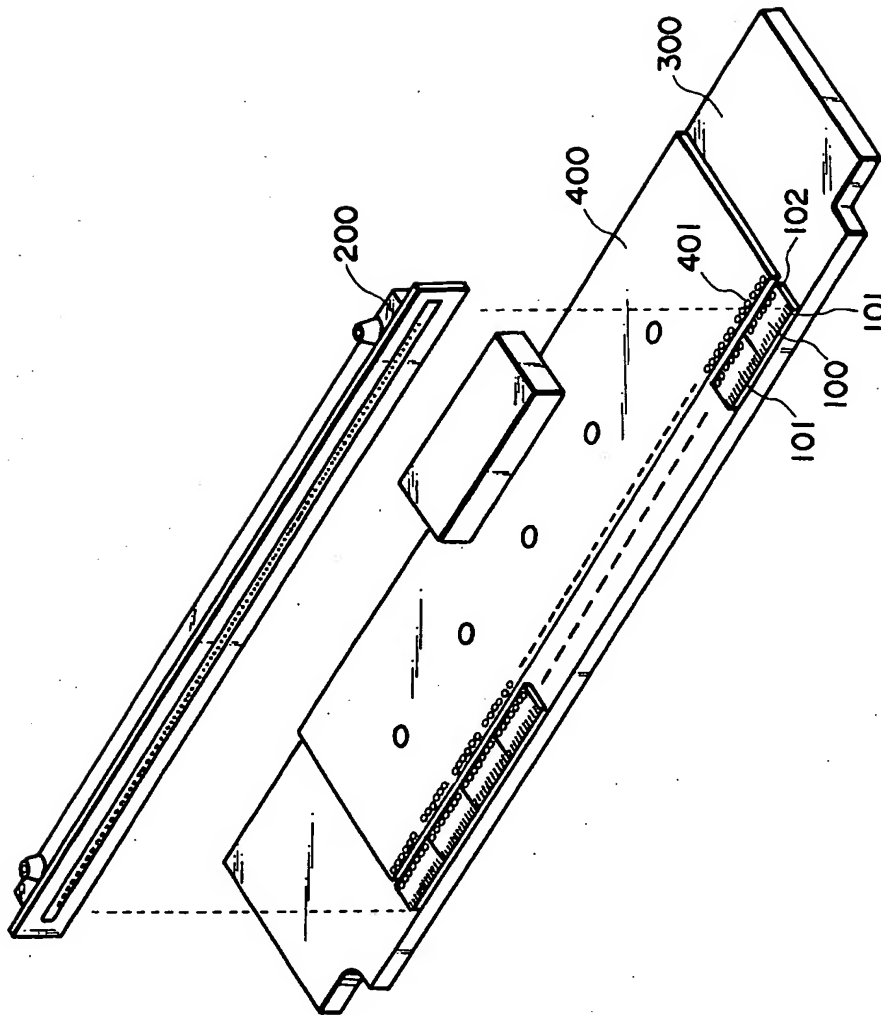


FIG. 6



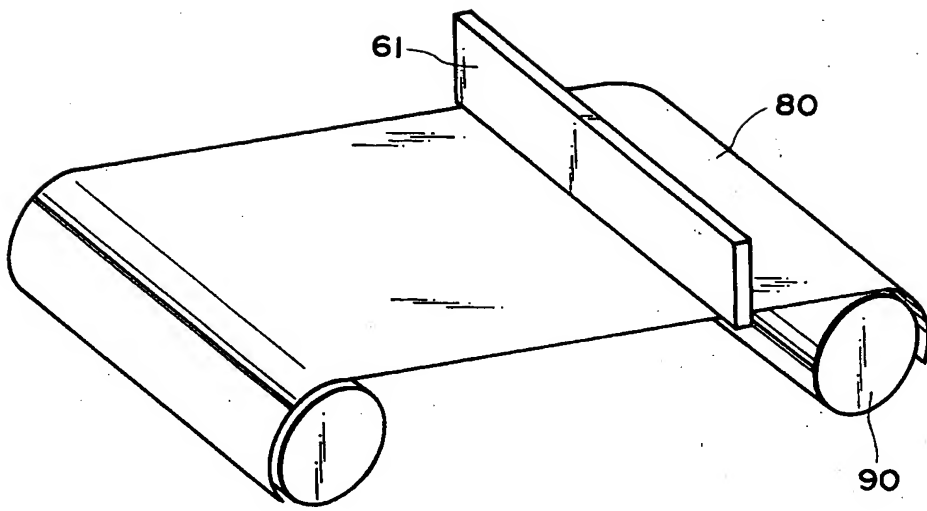


FIG. 8

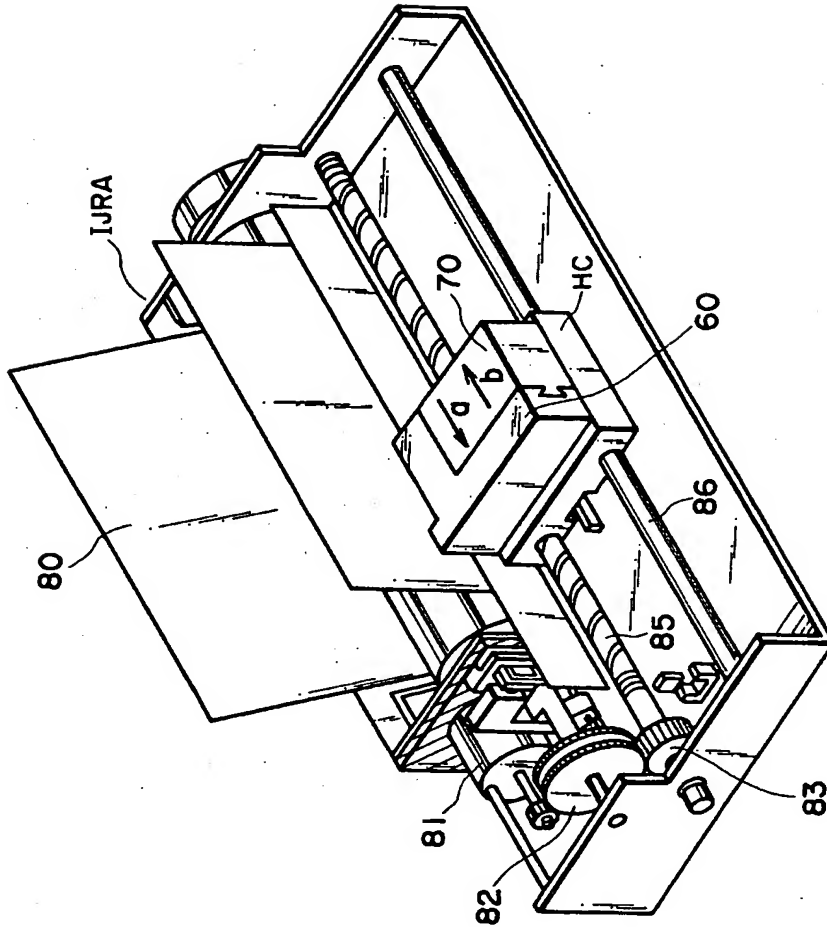


FIG. 9